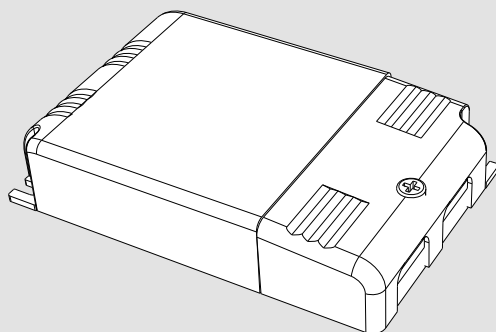
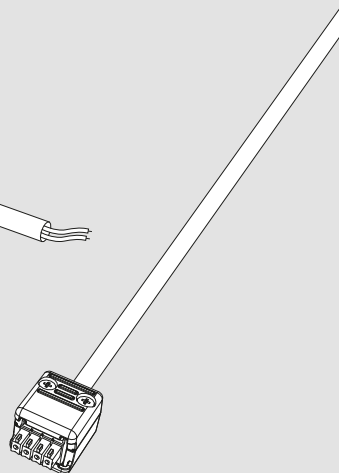
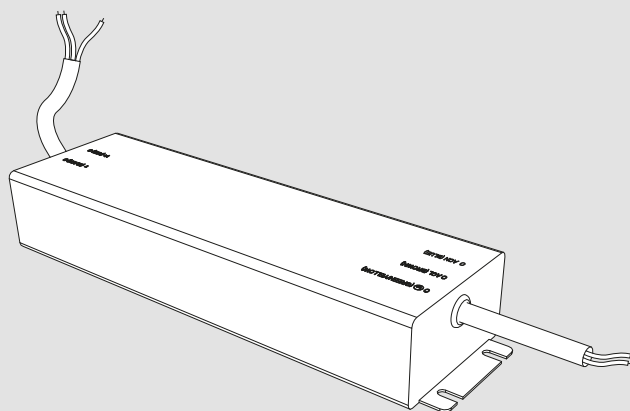


BARE POWER SUPPLY KIT A.24 DALI/0-10V/PUSH DART INTERFACE

design
C. de Bevilacqua



Magnetic



Diffused



Sharping

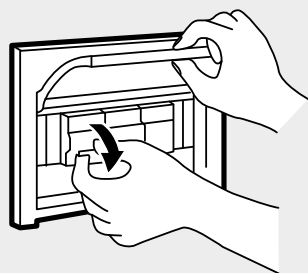
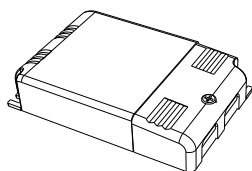
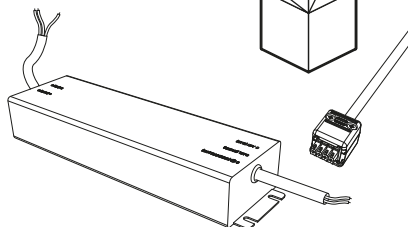
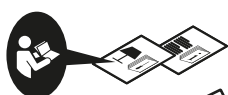
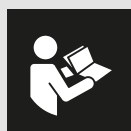
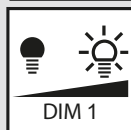
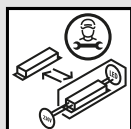
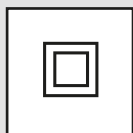




i

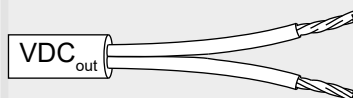
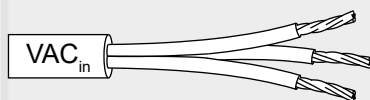
DALI/0-10V/PUSH-DART INTERFACE

48 VDC SELV POWER SUPPLY

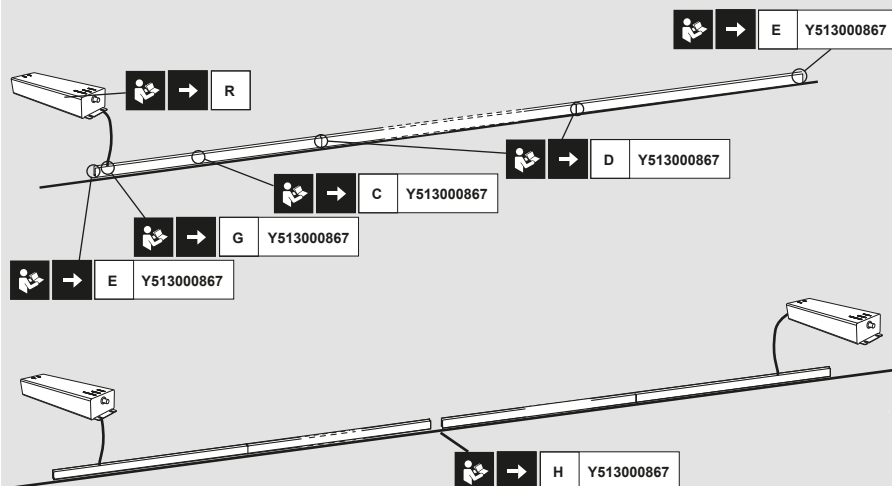


i

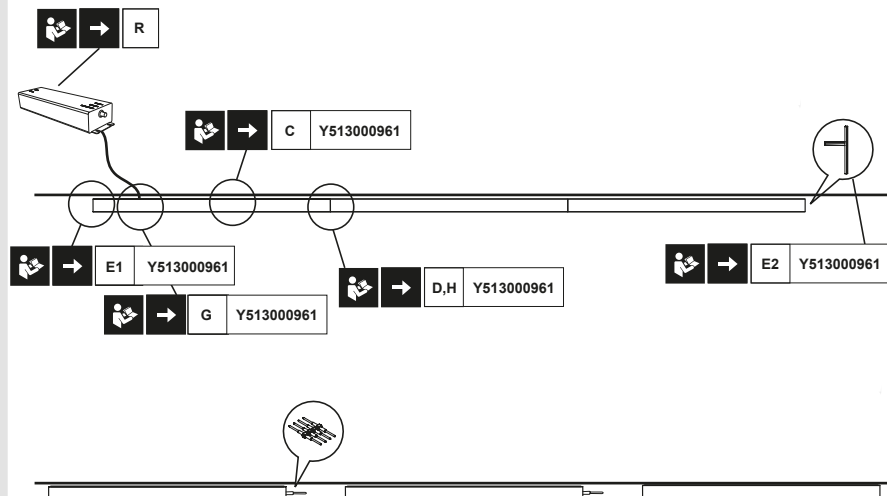
1 mm²
[AWG17]

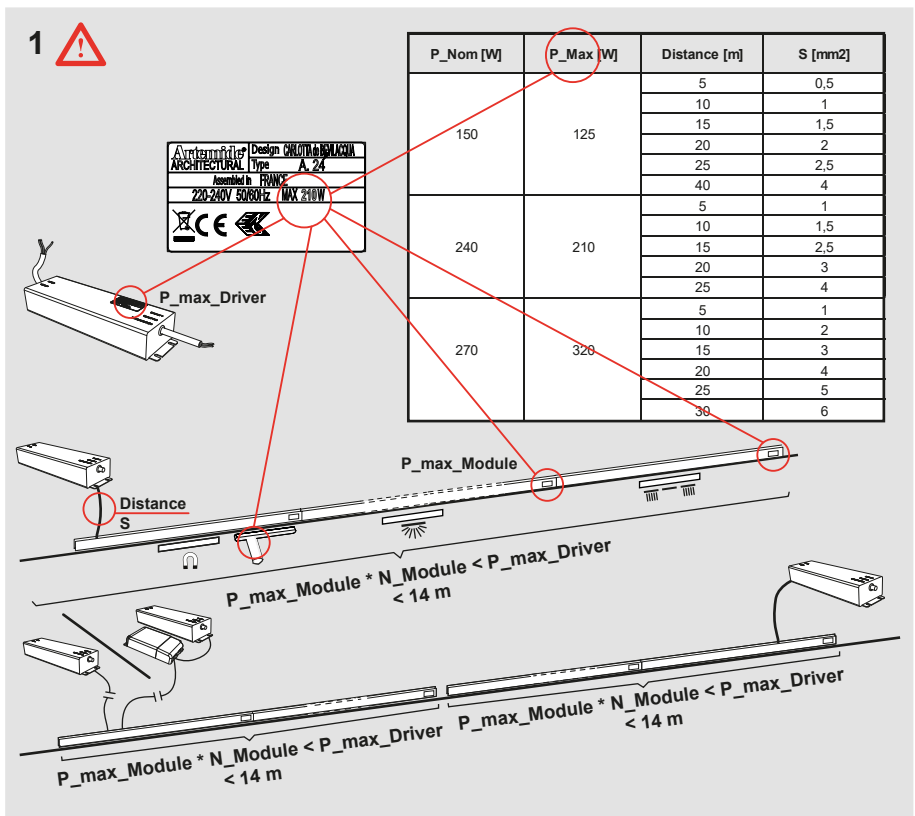
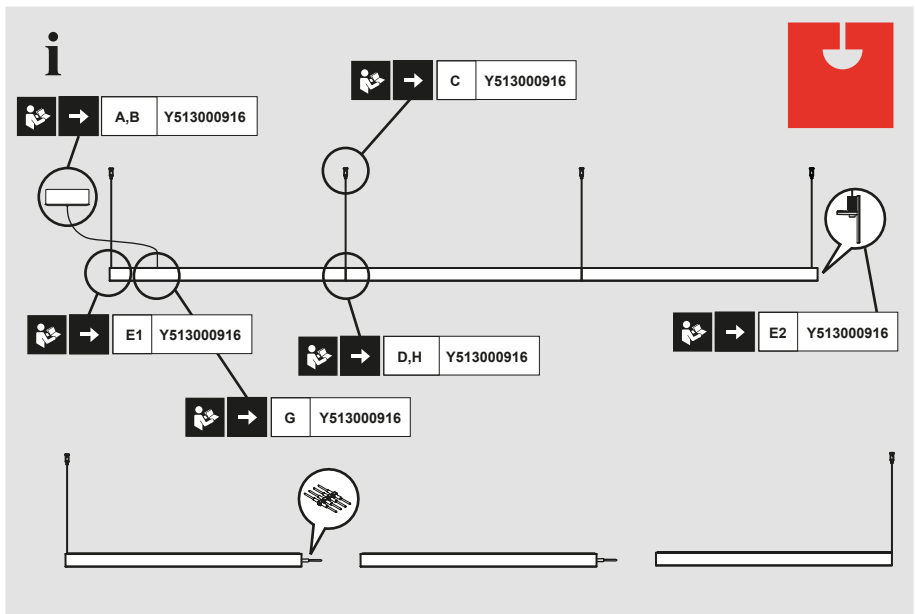


i



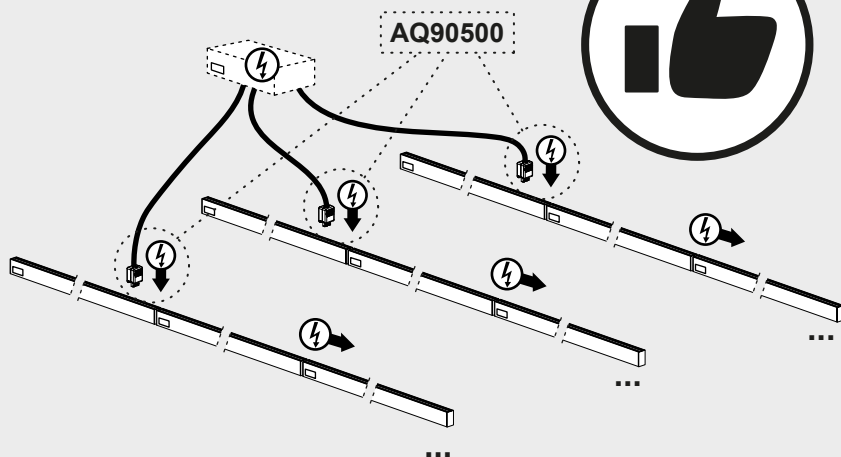
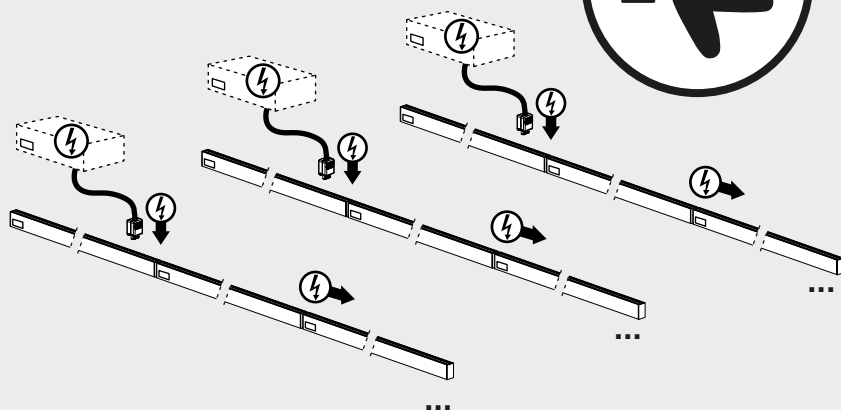
i







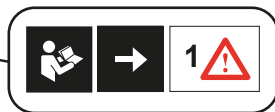
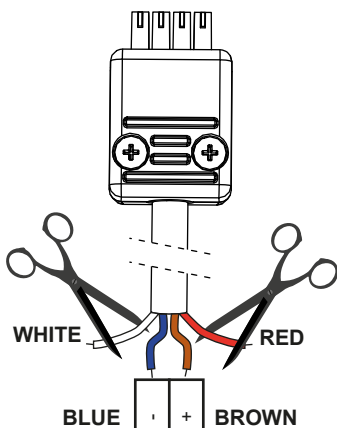
A.24 MULTIPLE SYSTEMS POWER SUPPLY



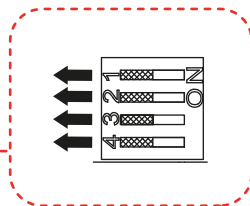
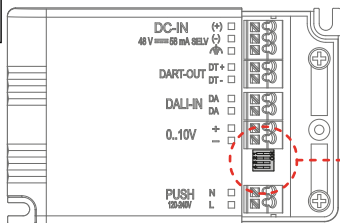
$$W_1 + W_2 + W_3 + \dots + W_N \leq \text{Output Max}$$

2a

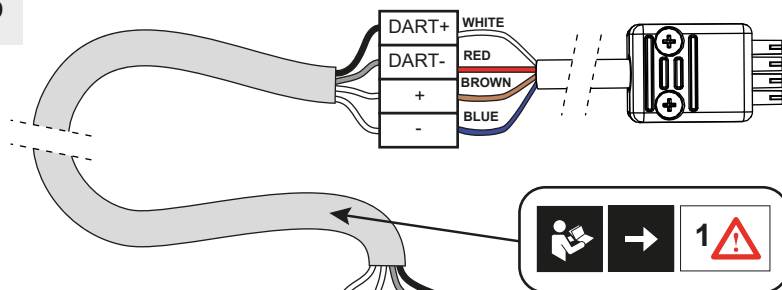
R



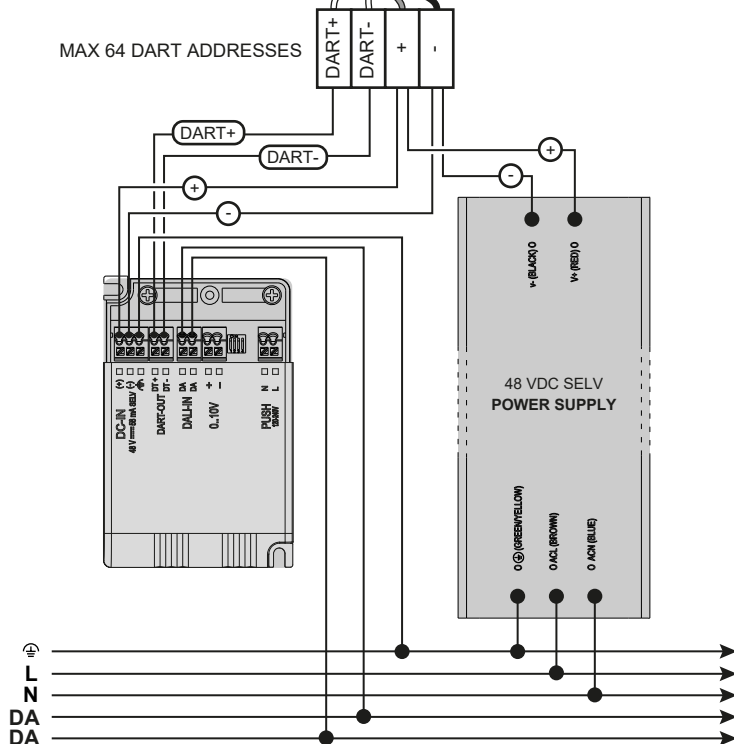
2b



3b

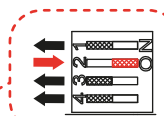
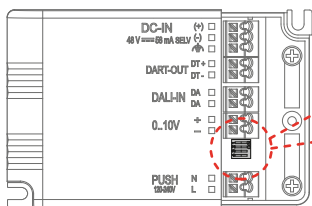


MAX 64 DART ADDRESSES

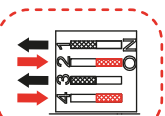


2d

R

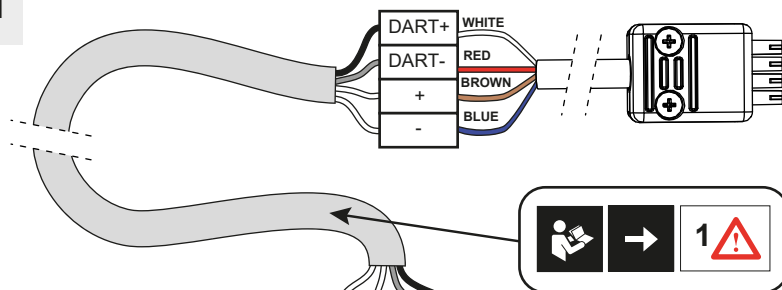


MIN. DIMMING
LEVEL 1%

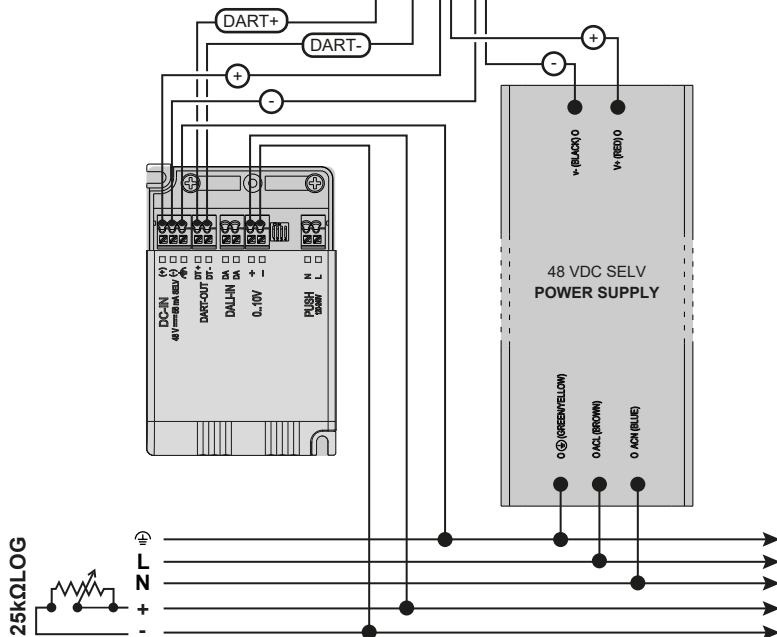


MIN. DIMMING
LEVEL 5%

3d



MAX 64 BROADCAST
DART ADDRESSES

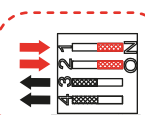
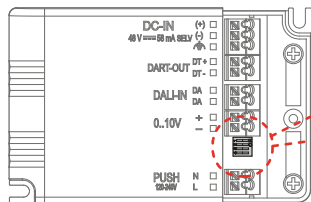


2e

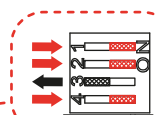
R



1-10V

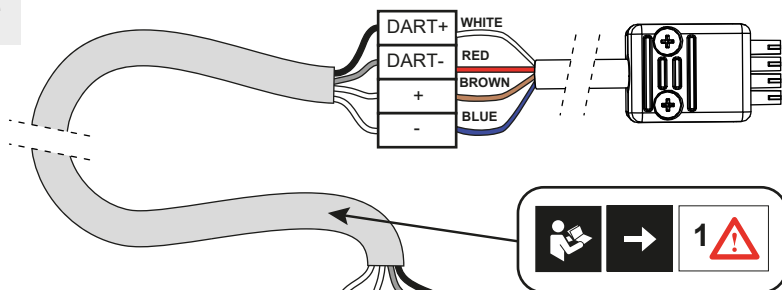


MIN. DIMMING
LEVEL 1%

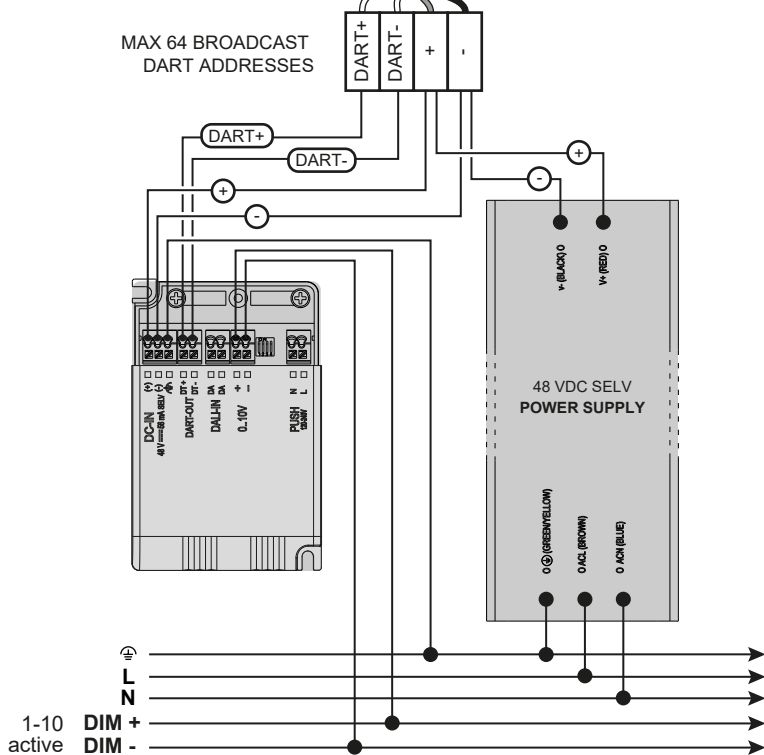


MIN. DIMMING
LEVEL 5%

3e



MAX 64 BROADCAST
DART ADDRESSES

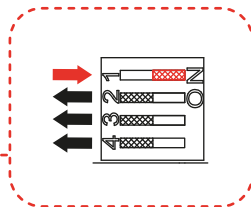
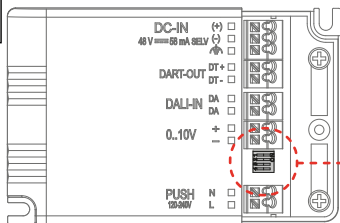


2f

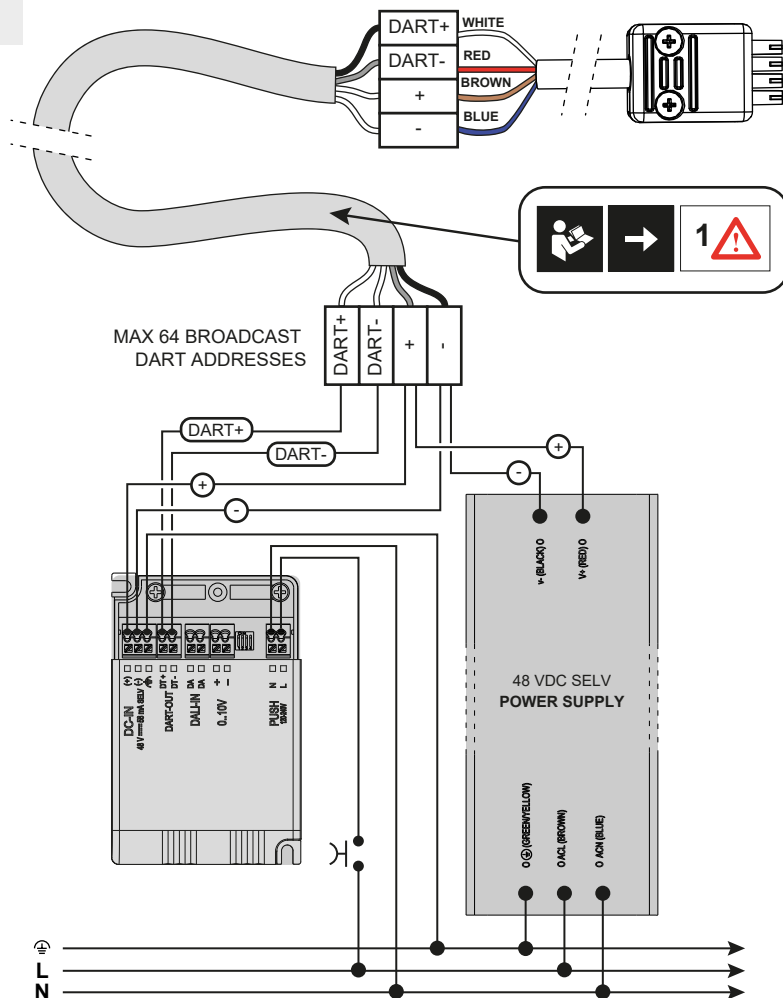
R



Push



3f

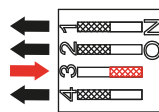
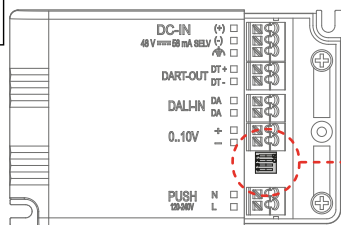


2g

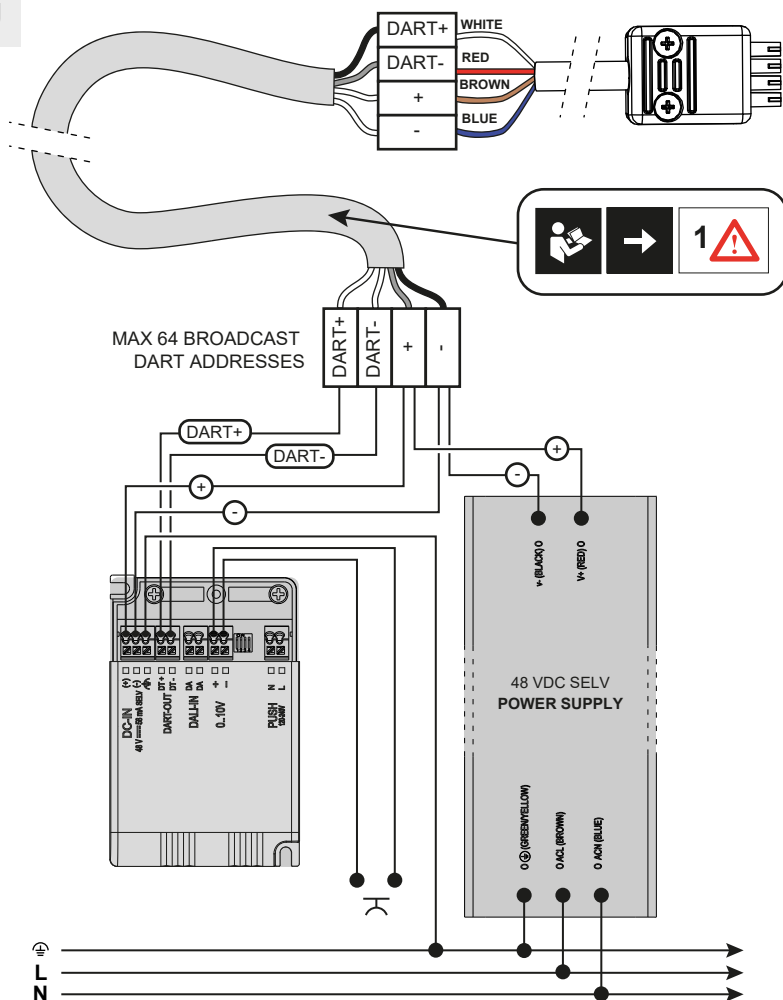
R



LV Push



3g





Insulation between circuits

Reinforced insulation between Push Button to SELV/DART-OUT

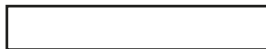
Supplementary insulation between DALI-IN to SELV/DART-OUT

Supplementary insulation between 0-10 V control to SELV/DART-OUT

Supplementary insulation between LV Push to SELV/DART-OUT

Basic insulation between Push Button to DALI-IN/0-10V control

No insulation between DALI-IN to 0-10V control (only one of these controls can be connected to the dimming)



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